

Application number 10/022,012  
Amendment dated 8/13/2003  
Reply to office action mailed April 15, 2003

PATENT

REMARKS/ARGUMENTS

After entry of this amendment, claims 1-29 will be pending in this application. Claims 20-22 have been amended. Claims 21 and 22 have been amended for consistency with claim 20. New claims 26-29 have been added. Support for the new and amended claims can be found in the specification. No new matter has been added.

Claims 20, 23, and 24 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,097,250 to Kamali et al. Claims 21 and 22 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Kamali. Reconsideration of these rejections and allowance of the remaining pending claims is respectfully requested. Claims 1-19 and 25 have been allowed.

Claim 20

Claim 20 stands rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,097,250 to Kamali et al. (hereafter Kamali). But Kamali does not teach each and every element of this claim. For example, claim 20, as amended, recites "wherein, the first differential output amplifier comprises a first differential pair and is configured to generate a first single-ended output signal and the second differential output amplifier comprises a second differential pair." Kamali does not provide these features.

Kamali appears to disclose an amplifier circuit. This amplifier circuit is formed in part by gain blocks G1, G2, and G3, which could be manufactured by Anadigics, Inc. as part number ACA0861DS7CTR. (Kamali at col. 2, lines 52-55 and Figures 4 and 5). A circuit diagram of such a gain block is depicted at Figure 3 of Kamali.

As shown in Figure 3 of Kamali, the gain block includes two separate, single-stage, common-source amplifiers. One amplifier includes common-source amplifier transistor Q<sub>1</sub>, which is connected to pass transistor Q<sub>3</sub>. The other amplifier in the gain block includes common-source amplifier transistor Q<sub>2</sub>, which is connected to pass transistor Q<sub>4</sub>. Accordingly, the two separate, single-stage amplifiers shown in

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Figure 3 of Kamali do not form a differential pair. Therefore, Kamali does not disclose a first differential output amplifier, which "comprises a first differential pair" or a second differential output amplifier, which "comprises a second differential pair" as required by the claim. On the contrary, the amplifiers A3-A4 and A5-A6 which make up gain blocks G1, G2, and G3, respectively, are separate, single-stage, common-source amplifiers.

For at least these reasons, claim 20 should be allowed.

Other claims


Claims 21-24 depend on claim 20, and should be allowed for at least similar reasons as claim 20, and for the additional limitations they recite.

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this application are in condition for allowance. The issuance of a formal notice of allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-752-2456.

Respectfully submitted,

  
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